

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Accompanying Divisional Application under 37 CFR 1.53:

Prior Application: N. KATOH et al
Serial No. 09/582,327
Filed: June 23, 2000

Group Art Unit: 2819
Examiner: A. Tran
For: SEMICONDUCTOR INTEGRATED CIRCUIT
DEVICE, STORAGE MEDIUM ON WHICH CELL
LIBRARY IS STORED AND DESIGNING METHOD
FOR SEMICONDUCTOR INTEGRATED CIRCUIT

PRELIMINARY AMENDMENT ✓

Assistant Commissioner of Patents
Washington, D.C. 20231

Sir:

Prior to examination, please amend the above application
as follows.

IN THE CLAIMS

Please rewrite claims 22 and 23 as set forth below.

BT
22. (Amended) A designing method for a semiconductor integrated circuit device according to claim 21, using the storage medium on which there is stored the cell library, comprising at least the steps of: